WYOMING DEPARTMENT OF TRANSPORTATION
ROAD DESIGN MEMORANDUM #03
DATE OF ISSUE: December 01, 2004

Approved by: Paul P. Bercich
Paul P. Bercich, P.E.
Highway Development Engineer

Issued by: Engineering Services, WYDOT, Cheyenne

GENERAL TOPIC: SURFACING
SUBJECT: CHIP SEALS

General

The following guidelines are required when Chip Seals (Referenced as Cover Coat Seals in the 1996 Specifications) are used on two-lane highways. These guidelines take into account several newly implemented polices, such as bicycle routes, rumble strips, diverse opinions on maintenance of roadways, and the Department's continued philosophy to conserve materials. *Per reference to MEMORANDUM dated April 12, 2002, from Chief Engineer, Delbert McOmie, P.E.*

Requirements for Chip Seals–Two Lane Highways

In general, chip seals are not to be applied over rumble strips. The application width of chip seals must meet the following requirements:

1. Highways with shoulders less than 4 ft [1.2 m] in width: Chip seals may be applied full width (including shoulders), providing there are no rumble strips. When rumple strips exist the width of the chip seal is limited to the field-verified width between the inside edges of the rumble strips.

2. Highways with shoulders 4 ft [1.2 m] or greater in width: Chip seals are not to extend beyond the inside edges of rumble strips or exceed the width of the traveled lanes (including auxiliary lanes) plus 1 ft [0.3 m] on both shoulders, whichever is the minimum width. In special cases (where there are no rumble strips), the chip seals may be applied full width when recommended by the district, with concurrence (in writing) from the Materials Program. In this case justification for a full width chip seal must be related to the pavement performance, such as minimizing aggregate stripping, etc. Note that travel lane widths referred to above are design widths (typically 12 ft [3.6 m]), not as striped in the field.

Any deviation from the above guidelines requires approval by the Engineering and Planning Engineer.